





#### NPN SMALL SIGNAL SURFACE MOUNT TRANSIS

## **Features**

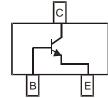
- **Epitaxial Planar Die Construction**
- Ideal for Medium Power Amplification and Switching
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device (Note 2)

### **Mechanical Data**

- Case: SOT-23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminals: Finish Matte Tin annealed over Alloy 42 leadframe. Solderable per MIL-STD-202, Method 208
- Marking & Type Code Information: See Page 2
- Ordering Information: See Page 2
- Weight: 0.008 grams (approximate)







Schematic and Pin Configuration

# **Maximum Ratings** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	60	V
Collector-Emitter Voltage	V <sub>CEO</sub>	50	V
Emitter-Base Voltage	V <sub>EBO</sub>	7.0	V
Collector Current - Continuous (Note 1)	Ic	150	mA

### Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3) @T <sub>A</sub> = 25°C	$P_D$	300	mW
Thermal Resistance, Junction to Ambient (Note 3) @T <sub>A</sub> = 25°C	$R_{ hetaJA}$	417	°C/W
Operating and Storage Temperature Range	$T_j$ , $T_{STG}$	-55 to +150	°C

# **Electrical Characteristics** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 4)								
Collector-Base Breakdown Voltage	V <sub>(BR)CBO</sub>	60		_	V	$I_C = 100 \mu A, I_E = 0$		
Collector-Emitter Breakdown Voltage	V <sub>(BR)CEO</sub>	50	_		V	$I_C = 1.0 \text{mA}, I_B = 0$		
Emitter-Base Breakdown Voltage	V <sub>(BR)EBO</sub>	7.0	_	_	V	$I_E = 50 \mu A, I_C = 0$		
Collector Cutoff Current	I <sub>CBO</sub>	_	_	100	nA	$V_{CB} = 60V, I_E = 0$		
Emitter Cutoff Current	I <sub>EBO</sub>	_	_	100	nA	$V_{EB} = 7.0V, I_{E} = 0$		
ON CHARACTERISTICS (Note 4)	ē.							
DC Current Gain	h <sub>FE</sub>	180	_	390		$V_{CE} = 6.0V, I_{C} = 1.0mA$		
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	_	0.2	0.4	V	$I_C = 50 \text{mA}, I_B = 5.0 \text{mA}$		
SMALL SIGNAL CHARACTERISTICS								
Output Capacitance	C <sub>obo</sub>	_	2.0	3.5	рF	$V_{CB} = 5V$ , $f = 1.0MHz$ , $I_E = 0$		
Current Gain-Bandwidth Product	f⊤	80	180	_	MHz	$V_{CE} = 12V, I_{C} = 2mA, f = 100MHz$		

Notes:

- No purposefully added lead. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead\_free/index.php.
- Device mounted on FR-4 PCB; pad layout as shown on page 2 or in Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.
- 4. Measured under pulsed conditions. Pulse width = 300μs. Duty cycle ≤2%.



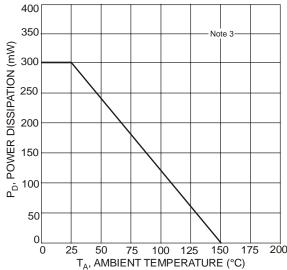


Fig. 1, Max Power Dissipation vs.
Ambient Temperature

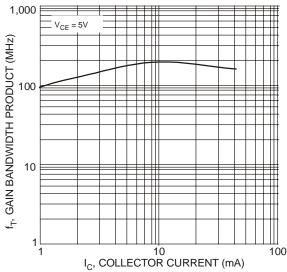


Fig. 3 Gain Bandwidth Product vs. Collector Current

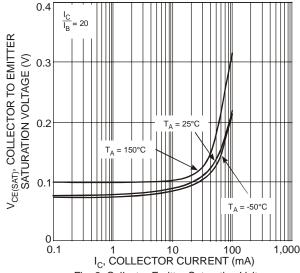
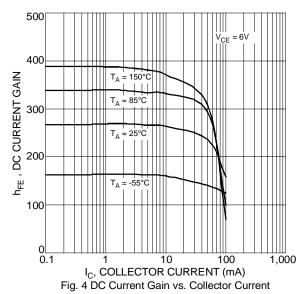


Fig. 2 Collector Emitter Saturation Voltage vs. Collector Current

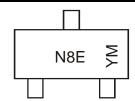


# Ordering Information (Note 5)

Device	Packaging	Shipping		
2DC2412R-7	SOT-23	3000/Tape & Reel		

Notes: 5. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

# **Marking Information**



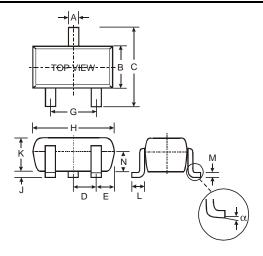
N8E = Product Type Marking Code YM = Date Code Marking Y = Year (ex: U = 2007) M = Month (ex: 9 = September)

Date Code Key

Year	20	07	20	08	20	09	20	10	20	11	20	12
Code	ι	J	\	/	V	٧	>	<	`	1	Z	<u>7</u>
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

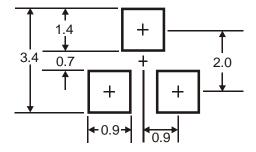


# **Package Outline Dimensions**



SOT-23							
Dim	Min	Max					
Α	0.37	0.51					
В	1.20	1.40					
С	2.30	2.50					
D	0.89	1.03					
E	0.45	0.60					
G	1.78	2.05					
Н	2.80	3.00					
J	0.013	0.10					
K	0.903	1.10					
L	0.45	0.61					
М	0.085	0.180					
N	-	_					
α	0°	8°					
All Dir	All Dimensions in mm						

# Suggested Pad Layout (Dimensions given in mm)



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